



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2419

SERIAL NUMBER 10/776,397	FILING DATE 02/10/2004 RULE	CLASS 438	GROUP ART UNIT 2822	ATTORNEY DOCKET NO. 2102397-992011
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Sohrab Kianian, Los Altos, CA;

Chih Hsin Wang, San Jose, CA;

** CONTINUING DATA ***** *JMJ*

This application is a DIV of 09/982,413 10/17/2001 PAT 6,917,069

** FOREIGN APPLICATIONS ***** *JMJ*

None

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/07/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>James M. Thomas</i> <i>JMJ</i> Examiner's Signature Initials	CA	18	24	2

ADDRESS

26379
 DLA PIPER RUDNICK GRAY CARY US, LLP
 2000 UNIVERSITY AVENUE
 E. PALO ALTO , CA
 94303-2248

TITLE

Self aligned method of forming a semiconductor memory array of floating gate memory cells with buried bit-line and vertical word line transistor

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
RECEIVED		